Digital Logic System 2020-2021

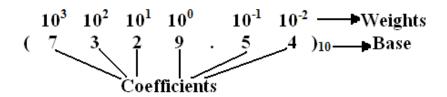
الدراسات الاولية /المرحلة الاولى / الفصل الاول أستاذ المادة مد.بشرى عبداللة سلطان

Lecture 1

1. Digital Computers:

The characteristic of the digital systems is its manipulation of discrete elements of information. Example discrete elements may be electrical impulses, the decimal digits, the letters of alphabet, arithmetic operations, punctuation marks and any other meaningful symbols. Discrete elements of information are represented in a digital system by physical quantities called signals. Electrical signals such as voltages are the most common. The signals in all present-day electronic digital system s have only two discrete values and are said to be binary.

2. Number Systems (Binary, Octal, Decimal and Hexadecimal):



Example:
$$(7329.54)_{10} = 10^3 * 7 + 10^2 * 3 + 10^1 * 2 + 10^0 * 9 + 10^{-1} * 5 + 10^{-2} * 4$$

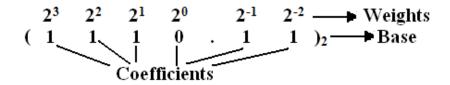
= $7000 + 300 + 20 + 9 + 0.5 + 0.04$

• When the **base** is equal to **10** the numbering system is named **Decimal** and the **coefficients range** is (0,1,2,3,4,5,6,7,8,9)

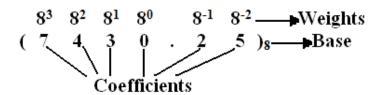
In general a number with decimal points is represented by a series of coefficients as follows:

$$(a_n....a_3\ a_2\ a_1\ a_0\centerdot a_{-1}\ a_{-2}\ a_{-3}......a_{-m})_{10}$$
 The a_j coefficients are one of the digits $(0,\ 1,\ 2,\ 3...,9)$
$$a_3*10^3_{+}\ a_2*10^2_{+}\ a_1*10^1_{+}\ a_0*10^0_{+}\ a_{-1}*10^{-1}_{+}\ a_{-2}*10^{-2}_{+}\ a_{-3}*10^{-3}$$

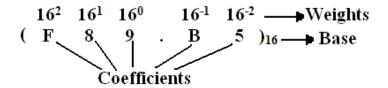
• The **Binary** number system is a different number system, the **coefficients** are (0 and 1) only and the base or radix 2, Ex:



• When the **base** is equal to **8** the numbering system is named **Octal** and the **coefficients range** is (0, 1, 2, 3, 4, 5, 6, 7), Ex:



• When the **base** is equal to **16** the numbering system is named **Hexadecimal** and the **coefficients range** is (**0**, **1**, **2**, **3**, **4**, **5**, **6**, **7**, **8**, **9**, **A**, **B**, **C**, **D**, **E**, **F**), where A=10, B=11, C=12, D=13, E=14, F=15. Ex:



In general a number expressed in base (r) system has coefficients (0, 1....r-1), multiplied by power of r

$$r^{n}*a_{n}+\ldots+r^{3}*a_{3}+r^{2}*a_{2}+r^{1}*a_{1}+r^{0}*a_{0}+r^{-1}*a_{-1}+r^{-2}*a_{-2}+\ldots-r^{-m}*a_{-m}$$

When the base of the number is less than (10) the needed (r) digit of the coefficients are borrowed from the decimal system. If the base is greater than (10) then the letters of the alphabet are used.

Lecture 2

3. Conversion from Decimal to Other Bases and vice versa.

3. A The conversion from any base r to decimal:

A number expressed in base r can be converted to its decimal equivalent by multiplying each coefficient with corresponding power of r and adding.

$$7^{2}$$
 7^{1} 7^{0} 7^{-1}
(6 3 0 . 4)₇ =6* 7^{2} +3* 7^{1} +0* 7^{0} +4* 7^{-1}
= 49*6+21+0+4/7= 294+21+0+0.571=(315.571)₁₀

3. B The Conversion from decimal to any base r:

Note: The conversion is more convenient if the number is separated into an integer part and a fraction part so the conversion of each part is done separately.

Ex: Convert the decimal number (14.625) to binary (base 2)

Integer	Remainder	
14÷2		
7÷2	0	a_0
3÷2	1	a_1
1÷2	1	a_2
0	1	a ₃

 $(14)_{10} = (1110)_2$

	Integer	Fraction	
		0.625	=1. 25
		* 2	-1. 23
a ₋₁	1	0.25 *2	=0.5
a ₋₂	0	0.5 *2	=1. 0
a ₋₃	1	0.00	

 $(0.625)_{10}$ = $(0.101)_2$ $(14.625)_{10}$ = $(1110.101)_2$

Ex: Convert the decimal number (315.571) to (base 7)

Integer	Remainder
---------	-----------

315÷7		
45÷7	0	\mathbf{a}_0
6÷7	3	a_1
0	6	\mathbf{a}_2

 $(315)_{10} = (630)_7$

	Integer	Fraction	
		0. 571 * 7	=3. 997
a ₋₁	3	0.997 *7	=6. 979
a ₋₂	6	0.979 *7	=6.853
a ₋₃	6	0.853	

$$(0.571)_{10} = (0.366)_7$$

 $(315.571)_{10} = (630.366)_7$

Ex: Convert the decimal number (314.21) to Hexadecimal (base 16)

 $a_0 \\ a_1 \\ a_2$

Integer	Remainder
314÷16	
19÷16	A
1÷16	3
0	1

 $(314)_{10} = (13A)_{16}$

	Integer	Fraction	
		0. 21	=3. 36
		* 16	-3. 30
a_{-1}	3	0.36	=5. 76
	J	*16	-3.70
a_{-2}	5	0.76	=12.16
	3	*16	-12.10
a_3	C	0.16	
	Ò		

$$(0.21)_{10}$$
= $(0.35C)_{16}$
 $(314.21)_{10}$ = $(13A.35C)_{16}$

HW. Convert the following number to the indicated bases

1. (214.3)₁₀ to base 4.

- 2. $(10101.101)_2$ to decimal.
- 3. $(124.03)_5$ to base 7. **Hint.** $(124.03)_5 = (?)_{10} = (?)_7$
- 4. $(346.67)_{10}$ to base 16.
- 5. $(124.34)_{10}$ to base 12.
- 6. $(110101.1101)_2$ to decimal.
- 7. $(42F.CB)_{16}$ to decimal
- 8. $(111011010.001101)_2$ to Octal.

Hint. $(111011010.001101)2 = (?)_{10} = (?)8$

9. (12A.8)12 to Decimal

Lecture 3

4. A number with different bases:

	imal	Binary					Octal		Hexadecimal	
(0,1)	,,9)	(0,1)					(0,1,7)		(0,1,9,AF)	
10^{1}	10^{0}	2^{4}	2^{3}	2^{2}	2^{1}	2^{0}	8^1	8^0	16 ¹	16^{0}
10	1	16	8	4	2	1	8	1	16	1
	0					0		0		0
	1					1		1		1
	2				1	0		2		2
	3				1	1		3		3
	4			1	0	0		4		4

	5			1	0	1		5		5
	6			1	1	0		6		6
	7			1	1	1		7		7
	8		1	0	0	0	1	0		8
	9		1	0	0	1	1	1		9
1	0		1	0	1	0	1	2		A
1	1		1	0	1	1	1	3		В
1	2		1	1	0	0	1	4		C
1	3		1	1	0	1	1	5		D
1	4		1	1	1	0	1	6		Е
1	5		1	1	1	1	1	7		F
1	6	1	0	0	0	0	2	0	1	0

5. Octal, Hexadecimal and Binary numbers:

The conversion from and to binary (base 2), Octal (base 8) and hexadecimal (base 16) plays an important part in digital computers, since 2^3 =8 and 2^4 =16 each octal digit corresponds to 3-binary digits and each hexadecimal digit corresponds to 4-binary digits.

 $Ex:(1) (10110001101011.11110000011)2=(?)_8=(26153.7406)_8$

 $=(?)_{16}=(2C6B.F06)_{16}$ $110)_{2}$ **(0**10 110 001 101 011 111 000 100 **(2** 1 5 3 7 4 0

(00 10	1100	0110	1011	•	1111	0000	$0110)_2$
(2	C	6	В		F	0	6) ₁₆

(2) $(673.124)_8 = (?)_2 = (110111011.0010101)_2$

(6	7	3	•	1	2	4)8
(110	111	011	•	001	010	$100)_2$

(3)
$$(306.D)_{16} = (?)_2 = (1100000110.1101)_2$$

(3	0	6	•	$D)_{16}$
(0011	0000	0110	•	$1101)_2$

HW. Convert the following number to the indicated bases

- 1. (456.7)₈ to hexadecimal using the binary as intermediate base.
- 2. $(98FE.0AB)_{16}$ to Octal using the binary as intermediate base.
- 3. $(10AB.FE)_{16}$ to octal using the binary as intermediate base.

- 4. $(6754.231)_8$ to Hexadecimal using the binary as intermediate base
- 5. $(111011010.001101)_2$ to Octal.
- 6. $(AD09.3C)_{16}$ to Binary.

Lecture 4

6. Arithmetic Operation:

Addition (+), Subtraction (-), Multiplication (*)

Carry		(1)	(1)	(1)	(1)		
Augend	(1	0	1	1	0	$1)_{2}$	
Addend	(1	0	0	1	1	$1)_{2}$	+
Sum	(10	1	0	1	0	$0)_{2}$	

Borrow
$$0 \quad \sqrt{0} \quad 10$$
Minuend $(1 \quad 0 \quad \sqrt{1} \quad \sqrt{1} \quad \sqrt{0} \quad 1)_2$
Subtrahend $(1 \quad 0 \quad 0 \quad 1 \quad 1 \quad 1)_2 \quad -$
Difference $(0 \quad 0 \quad 0 \quad 1 \quad 1 \quad 0)_2$

HW. Perform the following operation without converting to decimal or any other base:

- 1. (1101.01 * 10.1)₂=
- 2. $(D39 + 16A)_{16}$ =
- 3. $(243.13 144.02)_5 =$
- 4. (11100.01 1110.11)₂=
- 5. (243 * 24)₅=
- 6. (FB09 ED32)₁₆=
- 7. $(227.65 + 624.31)_8 =$
- 8. $(FDOD AE21)_{16} =$
- 9. (323 * 32)₄ =

7. Complement and Subtraction using 1's and 2's Complement.

7.1 Complement:

Complements are used in digital computers for simplifying the subtraction operation and for logical manipulation. There are two types of complements for each base (r) system:

- I. The r's complements
- II. The (r-1)'s complements
- 7.1.1 The \mathbf{r} 's complements: A positive number \mathbf{N} in base \mathbf{r} with an integer part of \mathbf{n} digits, the \mathbf{r} 's complement of \mathbf{N} is defining as follows:

$$r^n$$
-N for N $\neq 0$
0 for N=0

Ex:

a) The 2's complement of $(101100)_2$ is N=101100, r=2, n=6 \rightarrow the 2's complement equal to: $(2^6)_{10}$ - $(101100)_2$ = $(1000000-101100)_2$ = $(10100)_2$

b) The 2's complement of $(0.0110)_2$ is N=0.0110, r=2, n=0 \rightarrow the 2's complement equal to : $(2^0)_{10}$ - $(0.0110)_2$ = $(1-0.0110)_2$ = $(0.1010)_2$

7.1.2 The (r-1)'s complements:

A positive number N in base r with an integer part of n digits and a fraction part of m digits, the (r-1)'s complement of N is define as follows:

$$r^n$$
- r^{-m} - N

a) The 1's complement of $(101100)_2$ is N=101100, r=2, n=6 ,m=0 \rightarrow the 1's complement equal to: $(2^6-2^0)_{10}$ - $(101100)_2$ = $(1000000-1)_2$ - $(101100)_2$

$$=(1111111-101100)_2=(10011)_2$$

b) The 1's complement of $(0.0110)_2$ is N=0.0110, r=2, n=0 ,m=4 \rightarrow the 1's complement equal to: $(2^0-2^{-4})_{10}$ - $(0.0110)_2$ = $(1-0.0001)_2$ - $(0.0110)_2$ =(0.1111-0.0110)2= $(0.1001)_2$

Lecture 5

7.2 Subtraction using Complement:

The direct method of subtraction taught in elementary schools uses the borrow concept. This method seems to be easiest when people perform subtraction with paper and pencil, but this method is found to be less efficient when it is implemented by means of digital computers.

7.2.1 Subtraction using r's Complement:

Digital Logic System Dr. Bushra A. Sultan

The subtraction of two positive numbers (M-N), both of base r, may be done as follows

- 1. M+(r's complement of N)=
- 2. Inspect the result for an end carry.
 - a. If carry occurs discard it.
 - b. If no carry the result is equal to –(r's complement of the result of step 1).

Ex. $(110101-100010)_2$ subtract using 2's complement.

$$(110101-100010)_2$$
= 110101+[2's complement of 100010]
=110101+[2⁶-100010]
=110101+[1000000-100010]₂
=(110101+0111110)₂= $\cancel{1}$ 010011 \rightarrow Result=(10011)₂ (rule 2.a)

H.W (100010-110101)₂ subtract using 2's complement.

7.2.2 Subtraction using (r-1)'s Complement:

The subtraction of two positive numbers (M-N), both of base r, may be done as follows:

- 1. M+[(r-1)'s complement of N]=
- 2. Inspect the result for an end carry.
 - a. If carry occurs, add (1) to the least significant digit (LSD).
 - b. If no carry the result is equal to -[(r-1)'s complement of the result of step 1).

Ex. (1101-1000.01)₂ subtract using 1's complement.

HW. Subtract the following number using

- 1. (1011.101 1111)₂ using 1's complement.
- 2. $(110011.101 1101.1)_2$ using 2's complement.

Lecture 6

8. Alphanumeric Codes:

Many applications of digital computers require handling name as well as letters. An alphanumeric code is a binary code of a group of elements consisting of 10 decimal digits, the 26 letter of alphabet, certain number of special symbols such as \$,;,',...etc. The total number of elements in an

alphanumeric group is >36. So it must be coded with minimum number of bits $(2^6=64)$

- ASCII code (American Standard Code for Information Interchange) 7-bit Length code.
- EBCDIC code (Extended **BCD** Interchange Code) 8-bit Length code.

Letters	ASCII code		Letters	ASCII code	
	decimal	binary		decimal	binary
A	65	100 0001	a	97	110 0001
В	66	100 0010	b	98	110 0010
:					
Z	90	101 1010	Z	122	111 1010
0	48	011 0000			
1	49	011 0001			
:					
9	57	011 1001			
\$	36	010 0100			
(40	010 1000			
?	47	010 1111			

9. Binary Logic and Gates

9.1 Binary logic:

Binary logic deals with the variables take two discrete values true and false, yes and no. Binary logic is used to describe, in a mathematical way, the manipulation and processing of binary information. It particularly suited

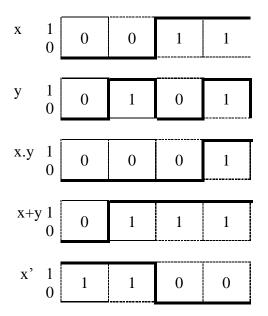
for analysis and design of digital systems. Binary Logic is equivalent to Boolean algebra.

9.2 Definition of Binary logic:

Binary logic consists of binary variables and logic operation. The variables are designated by letters e.g. A, B, C, r, w, x, y with only two distinct values: 1 and 0. The basic logical operators are AND, OR and NOT.

AND similar to * in binary arithmetic

OR similar to + in binary arithmetic but 1+1=10 (in binary arithmetic) 1+1=1 (in binary logic)



Timing diagram illustrate the response of each circuit to each of 4 input binary combinations (00,01,10,11)

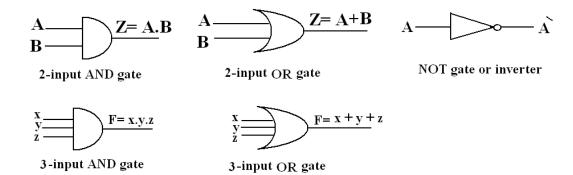
9.3 Logic Gates:

Digital Circuits, Switching Circuits, Logic Circuits and Logic Gates are the same. Gates are block of hardware that produces a logic-1 or logic-0 output signal if input logic requirement are satisfied.

	AND					
Inp	uts	Output				
X	y	x.y				
0	0	0				
0	1	0				
1	0	0				
1	1	1				

OR					
Inp	uts	Output			
X	у	x+y			
0	0	0			
0	1	1			
1	0	1			
1	1	1			

NOT				
Input	Output			
X	$\overline{\mathbf{X}}$			
0	1			
1	0			



The mathematical system of binary logic is better known as Boolean or switching algebra. This algebra is conveniently used to describe the operation of complex network of digital circuits. Designers of digital systems use Boolean algebra to transform circuits diagram to algebraic expression and vice versa.

10.Integrated Circuits:

Is a small silicon semiconductor crystal, called a chip, containing electrical components such as transistor, diodes, resistor and capacitor. The various components are interconnected inside the chip to form electronic circuits. The chip is mounted on a metal or plastic packaged, and connection are welded to external pins to form the IC. IC's come in two types:

1) Flat package 2) dual package

As the technology of ICs has improved, number of gates that can be put on a single silicon chip has increased considerably.

SSI chip contains less than 10 gates.

MSI chip contains between 10 to 100 gates.

LSI chip contains between 100 to 1000 gates.

VLSI chip contains between thousands of gates.

11.Basic Identity of Boolean Algebra:

	a	b	
1	X+0=X	X.1=X	Identity Element
2	X+1=1	X.0=0	
3	X+X=X	X.X=X	
$4 \forall X \in$	$X+\overline{X}=1$	$X.\overline{X}=0$	Complement
$B \exists \bar{X} \ s.t$			Definition
5	$\overline{\overline{X}}=X$		
6	X+Y=Y+X	X.Y=Y.X	Commutative Law
7	X+(Y+Z)=(X+Y)+Z	X.(YZ)=(XY).Z	Associative Law
8	X.(Y+Z)=(X.Y)+(X.Z)	X+(Y.Z)=(X+Y).(X+Z)	Distributive Law
9	$\overline{(X+Y)} = \overline{X}.\overline{Y}$	$\overline{(X.Y)} = \overline{X} + \overline{Y}$	Demorgan's
		,	Theorem
10	X+XY=X	X(X+Y)=X	Absorption
			Theorem

Lecture 7

12.Boolean Functions:

Boolean functions are formed from binary variables, logic operators and equal sign. The function value can be either 1 or 0:

Ex:
$$F1 = xy\bar{z}$$

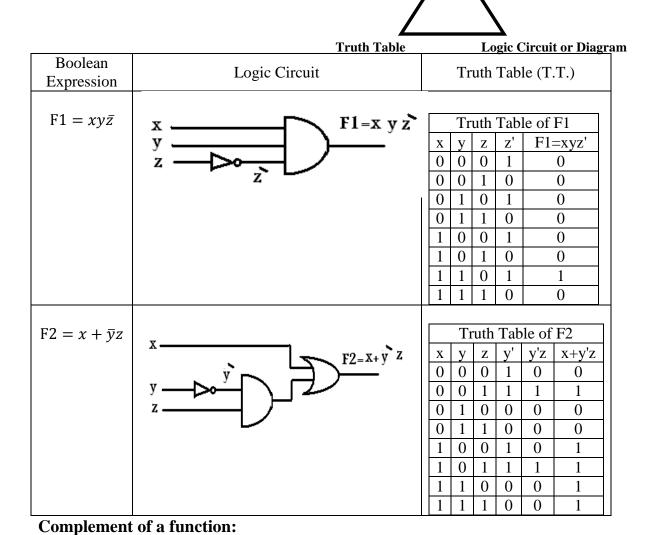
 $F2 = x + \bar{y}z$

A Boolean function also represented in truth table. A Boolean function may be transformed from an algebraic expression into a logic diagram or circuit composed of AND, OR and NOT get.

<u>NOTE</u>: The operator precedence for evaluating Boolean expression is:

1. Parentheses 2. NOT 3. AND 4. OR

Boolean Expression



The complement of any function F is F or \overline{F} its value can be obtained by interchange the (1's to 0's) and (0's to 1's) in the value of F. the complement of a function may be obtained algebraically through Demerger's theory.

Ex: Fined \overline{F} of F1, F2 and F3.

$$\overline{F1} = \overline{(xy\bar{z})} = \bar{x} + \bar{y} + \bar{\bar{z}} = \bar{x} + \bar{y} + z$$

$$\overline{F2} = \overline{(x + \bar{y}z)} = \bar{x}.\overline{\bar{y}z} = \bar{x}.(\bar{\bar{y}} + \bar{z}) = \bar{x}.(y + \bar{z})$$

$$\overline{F3} = \overline{AB} + C\overline{D} = \overline{AB}.\overline{CD} = (\bar{A} + \bar{B}).(\bar{C} + \bar{D}) = (A + \bar{B}).(\bar{C} + D)$$

13. Simplification of Boolean Functions using Boolean algebra.

<u>Literal:</u> is a single variable within the term that may be complemented or not. When a Boolean function is implemented with logic gates, each literal in Boolean function is designated an input to gate and each terms is implemented with gate. The minimization of a number of literal and the number of terms results in circuit with less equipment. Boolean algebra is a useful tool for simplifying digital circuit. Functions below simplified by using Boolean algebra.

Reduce the following Boolean expression using Boolean algebra to the indicated number of literals:

```
1) F1=x'yz+x'yz'+xy
                            to one literal
      = \underline{x'yz} + \underline{x'yz'} + xy
      =x'y(z+z')+xy
                           (Distributive law)
      =x'y.1+xy
                           (Complement definition)
      =x'y+xy
                          (Identity element)
      =y(x'+x)
                          (Distributive law)
      =y.1
                          (Complement definition)
    F1=y
                         (Identity element)
                        to two literals
2) F2=x+(x'y)
      =(x+x')(x+y)
                        (Distributive law)
      =1.(x+y)
                        (Complement definition)
```

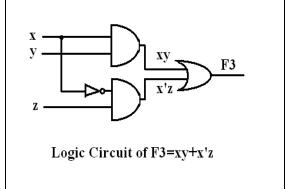
F2=x+y

3)
$$F3=xy+x'z+yz$$
 to four literals
= $xy+x'z+yz.1$ (Iidentity element)
= $xy+x'z+yz(x+x')$ (Complement definition)
= $xy+x'z+xyz+x'yz$ (Distributive law)

(Identity element).

$$=xy(1+z)+x'z(1+y)$$
 (Distributive law)
 $=xy.1+x'z.1$ ($x+1$)=1
 $F3=xy+x'z$ (Iidentity element)

Truth Table of $F3 = xy + x'z$								
X	Y	Z	x'	xy	x'z	F3=xy+x'z		
0	0	0	1	0	0	0		
0	0	1	1	0	1	1		
0	1	0	1	0	0	0		
0	1	1	1	0	1	1		
1	0	0	0	0	0	0		
1	0	1	0	0	0	0		
1	1	0	0	1	0	1		
1	1	1	0	1	0	1		

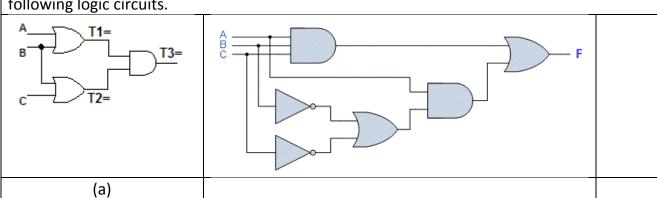


4)
$$F4 = x + y(z + \overline{x + z})$$
 to two literals
$$= x + y(z + \overline{x}\overline{z})$$
 (Demorgan's theory)
$$= x + y(z + \overline{x})(z + \overline{z})$$
 (Distributive law)
$$= x + y(z + \overline{x}).1$$
 (Complement definition)
$$= x + yz + y\overline{x}$$
 (Identity element)
$$= (x + y\overline{x}) + yz$$
 (Commutative law)
$$= (x + y).1 + yz$$
 (Distributive law)
$$= (x + y).1 + yz$$
 (Complement definition)
$$= x + y + yz$$
 (Identity element)
$$= x + y(1 + z)$$
 (Distributive law)
$$= x + y.1$$
 (Complement definition)
$$F4 = x + y$$
 (Identity element)

$$F5 = (AB + CD)(AB + C)$$
 to four literals is
 $= AB + (CD \cdot C)$ (Distributive law)
 $F5 = AB + CD$ (X · X=X)

HW.

Q1 Obtain the logical expression of T1, T2, T3 and F in term of the input variables, for the following logic circuits.



Q2) Reduce the following Boolean expression using Boolean algebra to the indicated number of literals:

1.
$$\overline{A}B(\overline{D} + \overline{C}D) + B(A + \overline{A}CD)$$

to one literal

2.
$$(A + \overline{A})(AB + AB\overline{C})$$

to two literals

3.
$$XY + XYZ + XY\overline{Z} + \overline{X}YZ$$

to three literals

to 3 literals

Q3) Draw the logic diagram of the following Boolean function without simplifying them

- 1. $F1 = B\overline{C} + AD$
- $2. F2 = B(\overline{C} + A)$
- 3. $F3 = \overline{(X+Y)}(\overline{X}+Y)$

Lecture 8

14. Canonical and Standard forms:

14. A Canonical form: canonical forms (Sum of Minterms or Product of Maxterms) are used to obtain the function from the given truth table

X	у	Z	Minterms	Notation	Maxterms	Notation
0	0	0	x'y'z'	m_0	(x+y+z)	M_0
0	0	1	x'y'z	m_1	(x+y+z')	M_1
0	1	0	x'yz'	m_2	(x+y'+z)	M_2
0	1	1	x'yz	m_3	(x+y'+z')	M_3
1	0	0	xy'z'	m_4	(x'+y+z)	M_4
1	0	1	xy'z	m_5	(x'+y+z')	M_5
1	1	0	xyz'	m_6	(x'+y'+z)	M_6
1	1	1	xyz	m_7	(x'+y'+z')	M_7
			Variable		Variable	
Primed if =0		Primed if =0		Primed if =1		
			Imprimed=1		Imprimed=0	

14. A .1 Sum of Minterms:

A Boolean function may be expressed algebraically as a sum of minterms from a given truth table by:

Step1: forming a minterm for each combination of the variables which produce 1 in the function.

Step2: OR all of the minterms in step1.

Example: From the given truth table express F as a sum of minterms

Given			en	Solution		
Iı	npu	ts	Output	Step1	Step2	
X	у	Z	F	minterms	Sum of minterms	
0	0	0	0		F = x'y'z + x'yz + xyz	
0	0	1	1	x'y'z	$F=m_1+m_3+m_7$	
0	1	0	0		$F(x,y,z)=\Sigma(1,3,7)$	
0	1	1	1	x'yz		
1	0	0	0			
1	0	1	0			
1	1	0	0			
1	1	1	1	xyz		

From the table F' can be expressed as a sum of minterms as follows:

Step1: forming a minterm for each combination of the variables which produce 0

in the function.

Step2: OR all of the minterms in step1.

Example: From the given truth table express F' as a sum of minterms

	Given		Solution			
Iı	npu	ts	Inputs	Step1	Step2	
X	у	Z	F	minterms	Sum of minterms	
0	0	0	0	x'y'z'	F'=x'y'z'+x'yz'+xy'z'+xy'z+xyz'	
0	0	1	1		$F'=m_0+m_2+m_4+m_5+m_6$	
0	1	0	0	x'yz'	$F'(x,y,z)=\Sigma(0,2,4,5,6)$	
0	1	1	1			
1	0	0	0	xy'z'		
1	0	1	0	xy'z		
1	1	0	0	xyz'		
1	1	1	1			

14. A .2 Product of Maxterms:

A Boolean function may be expressed algebraically as a product of maxterms from a given truth table by:

Step1: forming a maxterms for each combination of the variables which produce 0 in the function.

Step2: form the AND of all the maxterms in step1.

Example: From the given truth table express F as a product of maxterms

	Given			Solution	
Iı	npu	ts	Inputs	Step1	Step2
X	y	Z	F	maxterms	Product of maxterms
0	0	0	0	(x+y+z)	F=(x+y+z)(x+y'+z)(x'+y+z)(x'+y+z')(x'+y'+z)
0	0	1	1		$F = M_0.M_2.M_4. M_5.M_6$
0	1	0	0	(x+y'+z)	$F(x,y,z)=\Pi(0,2,4,5,6)$
0	1	1	1		
1	0	0	0	(x'+y+z)	
1	0	1	0	(x'+y+z')	
1	1	0	0	(x'+y'+z)	
1	1	1	1		

From the table F' can be expressed as follows:

Step1: forming a maxterm for each combination of the variables which produce 1 in the function.

Step2: form the AND of all the maxterms in step1.

Example: From the given truth table express F' as a product of maxterms

	Given		Solution			
Iı	npu	ts	Inputs	Step1	Step2	
X	у	Z	F	maxterms	Product of maxterms	
0	0	0	0		F'=(x+y+z')(x+y'+z')(x'+y'+z')	
0	0	1	1	(x+y+z')	$F = M_1.M_3.M_7$	
0	1	0	0		$F'(x,y,z) = \Pi(1,3,7)$	
0	1	1	1	(x+y'+z')		
1	0	0	0			
1	0	1	0			
1	1	0	0			
1	1	1	1	(x'+y'+z')		

14. B. Standard Forms:

Sum terms: single variable or logical sum of several variables such as (A, B, (x+y), (A+C')).

Product terms: single variable or logical product of several variables such as (x, y, AB', CD')

Note: the expression x+y'z (not sum term nor product terms)

14. B.1. Sum of Products (SOP): is a Boolean expression containing AND terms, called product terms, of one or more literals each. The sum denotes the ORing of these terms. An example of a function expressed as a sum of product is

$$F1=y'+xy+x'yz'$$

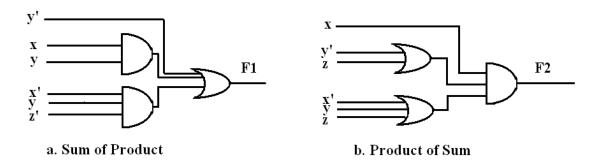
The expression contains three product terms (y' one literal, xy two literals and x'yz' three literals). Their sum is in effect an OR operation. The logic diagram if a sum-of-product expression consist of a group of AND gates followed by a single OR gate.

14. B.2 Product of Sums (POS): is a Boolean expression containing OR terms, called sum terms, of one or more literals each. The product denotes the ANDing of these terms. An example of a function expressed as a product of sum is

$$F2=x(y'+z)(x'+y+z)$$

The expression contains three sum terms (x one literal, y'+z two literals and x'+y+z three literals). The product is an AND operation. The logic diagram if a product-of-sum expression consist of a group of OR gates followed by a single AND gate.

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Two-level implementation

Note: a Boolean function may be expressed in a nonstandard form

Ex: F = (AB + CD) (A'B' + C'D') is neither in sum of products nor product of sums. It can be change to the standard form using the distributive law:

F = ABA'B' + ABC'D' + CDA'B' + CDC'D'

= 0 + ABC'D' + A'B'CD + 0

F = ABC'D' + A'B'CD sum of products

Lecture 9

Example: From the given truth table express F as a sum of minterms then simplify as a sum of product

Given			
X	у	Z	F
0	0	0	0
0	0	1	0
0	1	0	1
0	1	1	1
1	0	0	0
1	0	1	0
1	1	0	1
1	1	1	0

Solution: the function equal to 1 in $\{(2,x'yz'),(3,x'yz),(6,xyz')\}$

So F=x'yz'+x'yz+xyz' (Sum of Minterms)

Simplification of F

 $F = \underline{x'yz'} + \underline{x'yz} + xyz'$

=x'y(z'+z)+xyz' (distributive law)

=x'y.1+xyz' (complement definition)

=x'y+xyz' (identity element)

=y(x'+xz') (distributive law)

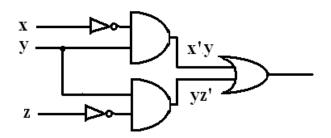
=y(x'+x)(x'+z') (distributive law)

=y.1.(x'+z') (complement definition)

=y(x'+z') (identity element)

=x'y+yz' (distributive law)

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Logic Circuit of F = x'y + yz'

Example: From the given truth table express F as a product of maxterms then simplify as a product of sum

Given			
X	У	Z	F
0	0	0	0
0	0	1	0
0	1	0	1
0	1	1	1
1	0	0	0
1	0	1	0
1	1	0	1
1	1	1	0

Solution: the function equal to 0 in $\{(0,x+y+z),(1,x+y+z'),(4,x'+y+z),(5,x'+y+z'),(7,x'+y'+z')\}$

So
$$F=(x+y+z)(x+y+z')(x'+y+z)(x'+y+z')(x'+y+z')$$
 product of maxterms

Simplification of F

F = (x+y+z) (x+y+z') (x'+y+z) (x'+y+z') (x'+y'+z')

$$=[(x+y)+zz'][(x'+y)+zz')](x'+y'+z')$$
 (distributive law)

$$=[(x+y)+0][(x'+y)+0](x'+y'+z')$$
 (complement definition)

$$= (\underline{x+y}) (\underline{x'+y}) (x'+y'+z')$$
 (identity element)

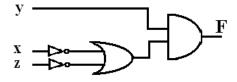
$$=(y+xx')(x'+y'+z')$$
 (distributive law)

$$= (y+0) (x'+y'+z')$$
 (complement definition)

$$= y(x'+y'+z')$$
 (identity element)

$$= x'y+yy'+yz'$$
 (distributive law)

$$=y(x'+z')$$
 (distributive law) to convert the function from SOP to POS



Logic Circuit of F=y(x'+z')

14.C convert functions to the canonical forms:

14.C.1 conversion to sum of minterms:

It is sometimes convenient to express function in its sum of minterms form by:

- a. Expanding the expression in to sum of AND terms (SOP)
- b. Each AND term is inspected to see if it contains all the variables. If it missing one or more variables, it is ANDed with an expression (x+x'), where x is one of the missing variable

Example: Express the Boolean function F=A+B'C in a sum of minterms.

Solution: the function F has three variables A,B and C. it is in SOP standard form the first product term (A) missing two variable (B,C); therefore

$$A=A(B+B')=AB+AB'$$

This terms still missing one variable (C):

$$A = AB(C+C') + AB'(C+C')$$

$$= ABC + ABC' + AB'C + AB'C'$$

The second term (B'C) missing one variable (A):

$$B'C = B'C (A+A') = B'CA + B'CA'$$
 rearrange the variable alphabetically

$$B'C = AB'C + A'B'C$$

Combining all terms, we have:

$$F = A + B'C$$

$$=ABC+ABC'+AB'C+AB'C'+AB'C+A'B'C$$

Since (x+x=x) we can eliminate one of the underlined term

F= A'B'C + AB'C' + AB'C + ABC' + ABC
=
$$m_1 + m_4 + m_5 + m_6 + m_7$$

F(A,B,C)= Σ (1,4,5,6,7)

14.C.2 conversion to product of maxterms:

To express function in its product of maxterms form by:

- a. Expanding the expression in to product of **OR** terms (POS), using distributive law
- b. Each OR term is inspected to see if it contains all the variables. If it missing one or more variables, it is **OR**ed with an expression (xx'), where x is one of the missing variable

Example: Express the Boolean function F = xy + x'z in a product of maxtermes.

First: convert the function into OR terms (POS) by using distributive law:

$$F = xy + x'z = let a = xy$$

$$F = a + x'z = (a + x') (a + z) = (xy + x') (xy + z)$$

$$= (x' + x) (x' + y) (z + x) (z + y)$$

$$F = (x' + y) (z + x) (z + y) POS$$

The function has three variables x, y and z. each OR term is missing one variable; therefore:

$$(x' + y) = (x' + y) + zz' = (x' + y + z)(x' + y + z')$$

 $(z + x) = (z + x) + yy' = (z + x + y)(z + x + y') = (x + y + z)(x + y' + z)$
 $(z + y) = (z + y) + xx' = (z + y + x)(z + y + x') = (x + y + z)(x' + y + z)$

Combining all terms and removing all those that appear more than once, we finally obtain:

F=
$$(x + y + z) (x + y' + z) (x' + y + z) (x' + y + z')$$

= $M_0 M_2 M_4 M_5$
F(x,y,z) = $\Pi (0, 2, 4, 5)$

14.C.3 Conversions between Canonical forms:

To convert from one canonical form to another: interchanging the symbols Π and Σ then list those numbers missing in the original form. In order to find the missing terms, one must realize the total number of minterms and maxterms is 2^n , where n is the number of binary variables in the function.

Example: convert the Boolean function $F(x,y,z) = \Sigma(0,2,4,5)$ to the other canonical form.

The number of variables is three (x, y, z), therefore the total numbers is in the range $(0..2^3-1)=(0..7)$ therefore, $F(x,y,z)=\Pi(1,3,6,7)$

Lecture 10 15.Other Logical Operations:

Name	Graphical symbol	Algebraic	Truth
		function	table
AND	y F	F=x.y	x y F 0 0 0 0 1 0 1 0 0 1 1 1
OR	$\begin{array}{c} x \\ y \\ \end{array}$	F=x+y	x y F 0 0 0 0 1 1 1 0 1 1 1 1
NOT, Inverter	x		x x' 0 1 1 0
Buffer	xx		x x 0 0 1 1
NAND	x y	$F = (xy)'$ $= x \uparrow y$	x y F 0 0 1 0 1 1 1 0 1 1 1 0

NOR	x y	$F=(x+y)'$ $= x \downarrow y$	x y F 0 0 1 0 1 0 1 0 0 1 1 0
XOR	x y	F=x'y+xy' =x⊕y	x y F 0 0 0 0 1 1 1 0 1 1 1 0
XNOR	x y	$F=x'y'+xy$ $=(x \oplus y)'$ $= x \odot y$	x y F 0 0 1 0 1 0 1 0 0 1 1 1

Exclusive OR (XOR): odd function equal to 1 if the numbers of the input variables have an odd number of 1's and 0 otherwise. The construction of 2-input Exclusive-OR function is shown below. It is normally implemented by cascading 2-input gates, as shown in below:

x y z	$F=x \oplus y \oplus z$	+
0 0 0	0	
0 0 1	1	y—))
0 1 0	1	$F = x \oplus y \oplus z$
0 1 1	0	z
1 0 0	1	Using 2-input gates
1 0 1	0	_
1 1 0	0	$F = x \oplus y \oplus z$
1 1 1	1	
		3 input gata
		3-input gate

The following identities apply to Exclusive –OR operation:

$$x \oplus 0=x$$
 , $x \oplus x=0$, $x \oplus y'=x' \oplus y=(x \oplus y)'$ $x \oplus 1=x'$, $x \oplus x'=1$,

Equivalence or Exclusive NOR: even function equal to 1 if the number of 0's in the input variables are even and 0 otherwise.

XOR and Equivalence are both commutative and associative.

$$x\uparrow y=(xy)'=x'+y'$$
 $y\uparrow x=(yx)'=y'+x'=x'+y'$ \therefore NAND is commutative

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$$x\downarrow y=(x+y)'=x'y'$$
 $y\downarrow x=(y+x)'=y'x'=x'y'$ \therefore NOR is commutative $(x\uparrow y)\uparrow z=(xy)'\uparrow z=((xy)'\ z)'=(xy)''+z'=xy+z'$ $x\uparrow (y\uparrow z)=x\uparrow (yz)'=(x'\ (yz)')'=x'+(yz)''=x'+yz$ \therefore NAND is not associative.

<u>H.W.</u> Neither prove that NOR is associative or not.

16.NAND and NOR gates.

Two other gates, NAND and NOR are often used in computer. To show any function can be implemented with NAND or NOR gates, we need only show that the logical operations of AND & OR can be obtained from NAND or NOR only.

	AND gate	OR gate
Using NAND	x(x.y)' = x.y	$ \begin{array}{c c} x & \xrightarrow{x'} \\ y & \xrightarrow{y'} \\ \end{array} $
Using NOR	$ \begin{array}{c} x \\ y' \end{array} $ $ \begin{array}{c} (x'+y')'=x'''.y''=x.y \end{array} $	x y $(x+y)'$ $(x+y)''=(x+y)$

HW.

Q1 Obtain the logical expression of T1, T2 and T3 in term of the input variables, for the following logic circuits.

